

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE STATEMENT**

Docket Number  
**10191/3283**

Application Number  
To Be Assigned

Filing Date  
Herewith

Examiner  
To Be Assigned

Art Unit  
To Be Assigned

Title  
**METHOD FOR PRODUCING INTEGRATED  
MICROSYSTEMS**

Applicant(s)  
**Wilhelm FREY et al.**

Mail Stop PCT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant(s) hereby bring the following reference(s) to the attention of the Examiner. The reference(s) are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 7/3/2003

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|---|---|---------------------------------------|
| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANTS<br/>PTO FORM 1449</b> | <b>ATTY. DOCKET NO.</b><br><b>10191/3283</b>          | <b>SERIAL NO.</b><br>Not yet assigned |
|   | <b>APPLICANT(s)</b><br><br><b>Wilhelm FREY et al.</b> |                                       |
|   | <b>FILING DATE</b><br>Herewith                        | <b>GROUP</b><br>Not yet assigned      |

**U. S. PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | PATENT<br>NUMBER | PATENT<br>DATE | NAME        | CLASS | SUBCLASS | FILING<br>DATE |
|---------------------|------------------|----------------|-------------|-------|----------|----------------|
|                     | 6,210,988        | April 3, 2001  | Howe et al. |       |          |                |

**FOREIGN PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | DOCUMENT<br>NUMBER | DATE              | COUNTRY              | CLASS | SUB-<br>CLASS | TRANSLATION |    |
|---------------------|--------------------|-------------------|----------------------|-------|---------------|-------------|----|
|                     |                    |                   |                      |       |               | YES         | NO |
|                     | 38 74 411*         | October 15, 1992  | Fed. Rep. of Germany |       |               | Abs.        |    |
|                     | 0 292 057**        | November 23, 1988 | Europe               |       |               | Abs.        |    |

\*Described in Specification

\*\* Corresponds to German Patent No. 38 74 411.

**OTHER DOCUMENTS**

| EXAMINER<br>INITIAL |  | AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.  |
|---------------------|--|---|
|                     |  | Franke et al., "Post-CMOS Modular Integration of Poly-SiGe Microstructures Using Poly-Ge Sacrificial Layers", Solid-State Sensor and Actuator Workshop, Hilton Head, SC, June 2000, pp. 18-21 |
|                     |  |   |
|                     |  |   |

|   |                 |
|---|-----------------|
| EXAMINER  | DATE CONSIDERED |
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                 |